

If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,



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Date: 4/26/04



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Commissioner for Patents
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each of said attorneys being employed by Sun Microsystems; and

| | | | |
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| B. Noël Kivlin | Reg. No. 33,929 | Mark S. Williams | Reg. No. 50,658 |
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each of said attorneys or agents being a member or an associate of the firm of Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., as its attorney or agent for so long as they remain with such firm, with full power of substitution and revocation, to prosecute the application, to make alterations and amendments therein, to transact all business in the Patent and Trademark Office in connection therewith, and to receive any Letters Patent, and for one year after issuance of such Letters Patent to file any request for a certificate of correction that may be deemed appropriate.

Send all future correspondence to:

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Assignee of Interest

SUN MICROSYSTEMS, INC.
4150 Network Circle
Santa Clara, California 95054

Dated: March 29, 2004

By: Paul D. Sorkin
Paul D. Sorkin, Reg. No. 39,039
Assistant General Counsel

APPENDIX A

| MHKKG Ref | Sun Ref | Filing Date | Serial # | Title | Inventor(s) |
|-----------|----------|-------------|------------|--|--|
| 56000 | P4637 | 11/21/00 | 09/721,029 | Single Unit Office Management Center | Talagala, Whay, Wu, Rettberg |
| 37100 | P4244PSP | 10/30/00 | 60/244,463 | Array of IDE/ATA Disks Emulating SCSI or Fibre Channel Devices | Yatziv |
| 79600 | P5480 | 12/28/00 | 09/750,555 | Method, System, and Program Managing the Rate at Which Client Requests for a Service are Transmitted | Conner |
| 79700 | P5624 | 04/04/01 | 09/826,081 | Method, System, and Program for Enabling Communication Between Devices Using Different Address Formats | Rao |
| 79800 | P5781 | 01/30/01 | 09/772,626 | Method, System, Program, And Data Structures For Testing A Network System Including Input/Output Devices | DeRolf & Kirton |
| 79900 | P5786 | 03/08/01 | 09/802,229 | Method, System, and Program for Determining System Configuration Information | Albright, DeRolf, Gibson, Kirton, & McKenney |
| 80000 | P5791 | 04/04/01 | 09/826,080 | Method, System, and Program for Enabling Communication Between Devices Using Dynamic Addressing | Rao |
| 80100 | P5883 | 03/21/02 | 10/104,923 | Message Routing in a Torus Interconnect | Lee, Talagala, Chong, Wu, & Rettberg |
| 80800 | P6196 | 09/28/01 | 09/967,141 | XOR Processing Incorporating Error Correction Code Data Protection | Acton |
| 80900 | P7057 | 09/28/01 | 09/967,137 | Mapping of Nodes in an Interconnection Fabric | Whay S. Lee |
| 81000 | P8430 | 05/15/03 | 10/440,218 | Method, System, and Program for Allocating Storage Resources | Kumar & Taylor |
| 81100 | P8992 | 03/13/03 | 10/389,642 | Method, System, and Article of Manufacture for Fault Determination | Gibson, McKenney, Cadieux, & Kiser |
| 81200 | P6555 | 06/13/02 | 10/172,302 | System and Method for Isolating Faults in a Network | Wiley, Westminster, & Cadieux |
| 81300 | P6248PSP | 06/15/01 | 60/298,658 | SAN First Fault Isolation | Wiley, Munson, & Cadieux |
| 81301 | P6248 | 06/13/02 | 10/172,303 | System and Method for Rapid Fault Isolation in a Storage Area Network | Wiley, Munson, & Cadieux |
| 81400 | P8966 | 11/04/03 | 10/702,150 | Using and Generating Finite State Machines to Monitor System Status | Keyur B. Desai |
| 81500 | P5445 | 08/10/01 | 09/927,985 | Method, System, and Program for Managing Multiple Resources in a System | Carlson & da Silva |
| 81600 | P5533 | 04/30/01 | 09/846,801 | Data Integrity Monitoring Storage System | Talagala |
| 81700 | P5826 | 09/28/01 | 09/967,142 | Discovery of Nodes in an Interconnection Fabric | Lee & Mortensen |
| 81800 | P7513PSP | 02/05/02 | 60/355,611 | Integrated and Automated Storage Management | Amy Lynch |
| 81801 | P7513 | 02/04/03 | 10/357,554 | Method, System, and Program for Supporting a Level of Service for an Application | Amy Elizabeth Lynch & Mark A. Carlson |
| 81900 | P5866 | 06/29/01 | 09/896,507 | Method and Apparatus for Fault Isolation on Network Loops | Gavin Gibson, Sam Gibson, & Yuan Zhang |
| 82000 | P7200 | 06/29/01 | 09/896,662 | Method and Apparatus for Fault Isolation on Network Loops Using Low Level Error Counters | Gavin Gibson, Sam Gibson, & Yuan Zhang |

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Name (line 1) Gavin G. Gibson

Name (line 2) Sam Gibson

Name (line 3) Yuan Zhang

Execution Date

Month Day Year

Sept. 19&21, 2001

Second Party

Name (line 1)

Name (line 2)

Execution Date

Month Day Year

Receiving Party☐ Mark if additional names of conveying parties attached

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Address (line 3) Palo Alto

California

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Monica Ward, Reg. 40,696

Name of Person Signing



Signature

10/10/2001

Date

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FOR GOOD AND VALUABLE CONSIDERATION, the receipt, sufficiency and adequacy of which are hereby acknowledged, the undersigned, does hereby:

SELL, ASSIGN AND TRANSFER to **SUN MICROSYSTEMS, INC.** (the Assignee), a corporation organized and existing under and by virtue of the laws of the State of **Delaware**, and having its principal place of business at **901 San Antonio Road, Palo Alto, California 94303** their entire right, title and interest for the United States and all foreign countries, in and to any and all improvements which are disclosed in the application for United States Letters Patent, which has been executed by the undersigned, and is entitled, **METHOD AND APPARATUS FOR FAULT ISOLATION ON NETWORK LOOPS**, having a US Patent Office filing date of June 29, 2001 and Serial Number 09/896,507, such application and all divisional, continuing, substitute, renewal, reissue and all other applications for patent which have been or shall be filed in the United States and all foreign countries on any of such improvements; all original and reissued patents which have been or shall be issued in the United States and all foreign countries on such improvements; and specifically including the right to file foreign applications under the provisions of any convention or treaty and claim priority based on such application in the United States;

AUTHORIZE AND REQUEST the issuing authority to issue any and all United States and foreign patents granted on such improvements to the Assignee;

WARRANT AND COVENANT that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been or will be made to others by the undersigned, and that the full right to convey the same as herein expressed is possessed by the undersigned;

COVENANT that, when requested and at the expense of the Assignee, to carry out in good faith the intent and purpose of this assignment, the undersigned will execute all divisional, continuing, substitute, renewal, reissue, and all other patent applications on any and all such improvements; execute all rightful oaths, declarations, assignments, powers of attorney and other papers; communicate to the Assignee all facts known to the undersigned relating to such improvements and the history thereof; and generally do everything possible which the Assignee shall consider desirable for securing, maintaining and enforcing proper patent protection for such improvements and for vesting title to such improvements in the Assignee;

COVENANT AND AGREE that the above is and will be binding on the heirs,
assigns, representatives and successors of the undersigned and extend to the
successors, assigns and nominees of the Assignee.

By: Gavin G. Gibson Date: 9-19-2001
Gavin G. Gibson

By: _____ Date: _____
Sam Gibson

By: Yuan Zhang Date: 9-19-2001
Yuan Zhang

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